

METHOD AND APPARATUS FOR VERIFYING MEMORY CONTENTS

ABSTRACT OF THE DISCLOSURE

A system and method of verifying a content of a non-volatile reprogrammable
5 memory communicatively coupled to a microprocessor is disclosed. The method
comprises the steps of reading at least some of the data stored in the non-volatile
reprogrammable memory, computing a value related to contents of the non-volatile
reprogrammable memory, and comparing the value with a stored integrity value. The
apparatus comprises a microprocessor, a non-volatile reprogrammable memory
10 communicatively coupled to the microprocessor via a first communication path, the non-
volatile memory for storing microprocessor program instructions, and a logical module,
communicatively coupled to the non-volatile memory via a communication path
independent from the first communication path, the logical module for verifying the data
stored in the non-volatile reprogrammable memory by comparison of the contents of the
15 non-volatile reprogrammable memory with a stored integrity value.

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